FIG. 1

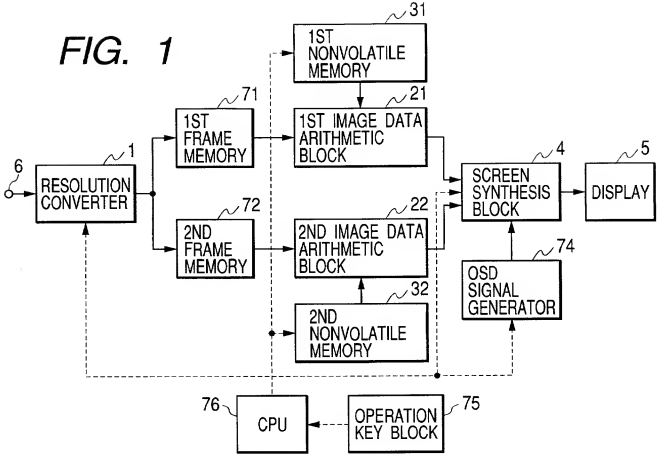

31
1ST NONVOLATILE MEMORY
71
1ST FRAME MEMORY
21
1ST IMAGE DATA ARITHMETIC BLOCK
6
1
RESOLUTION CONVERTER
4
SCREEN SYNTHESIS BLOCK
5
DISPLAY
72
2ND FRAME MEMORY
22
2ND IMAGE DATA ARITHMETIC BLOCK
74
OSD SIGNAL GENERATOR
32
2ND NONVOLATILE MEMORY
76
CPU
OPERATION KEY BLOCK
75

FIG. 2

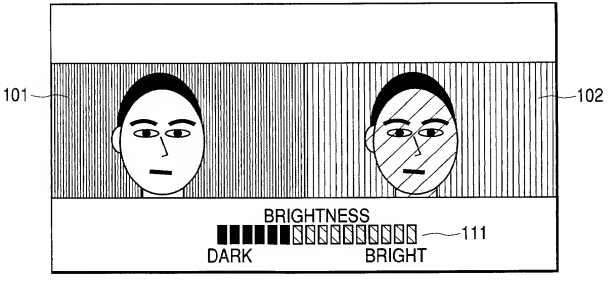

101
102
BRIGHTNESS
111
DARK
BRIGHT

FIG. 3

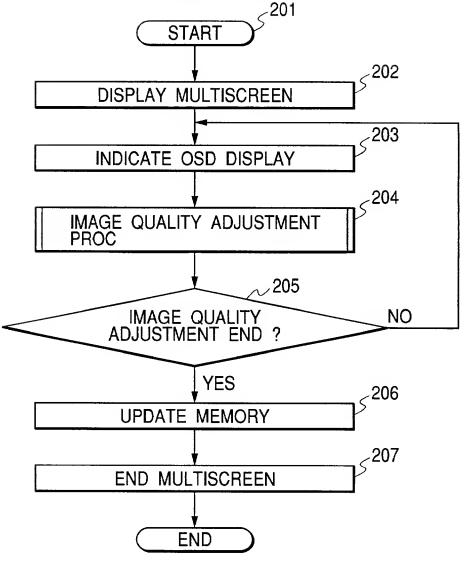

201
START
202
DISPLAY MULTISCREEN
203
INDICATE OSD DISPLAY
204
IMAGE QUALITY ADJUSTMENT PROC
205
IMAGE QUALITY ADJUSTMENT END ?
NO
YES
206
UPDATE MEMORY
207
END MULTISCREEN
END

FIG. 4

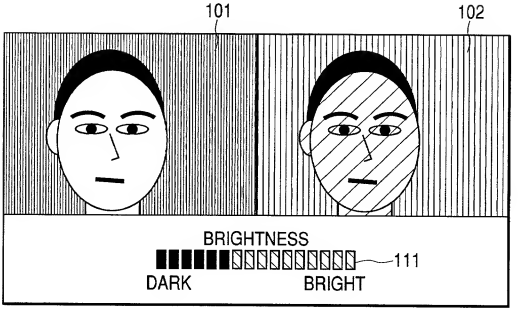

101
102
BRIGHTNESS
111
DARK
BRIGHT

FIG. 5

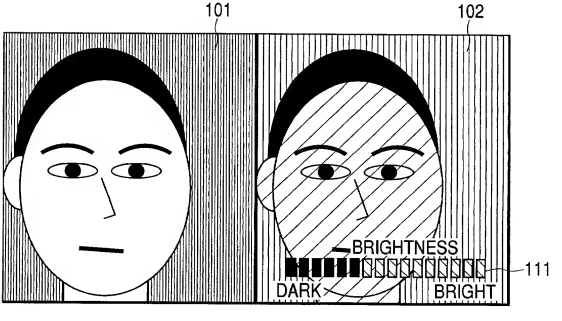

101
102
BRIGHTNESS
111
DARK
BRIGHT

FIG. 6

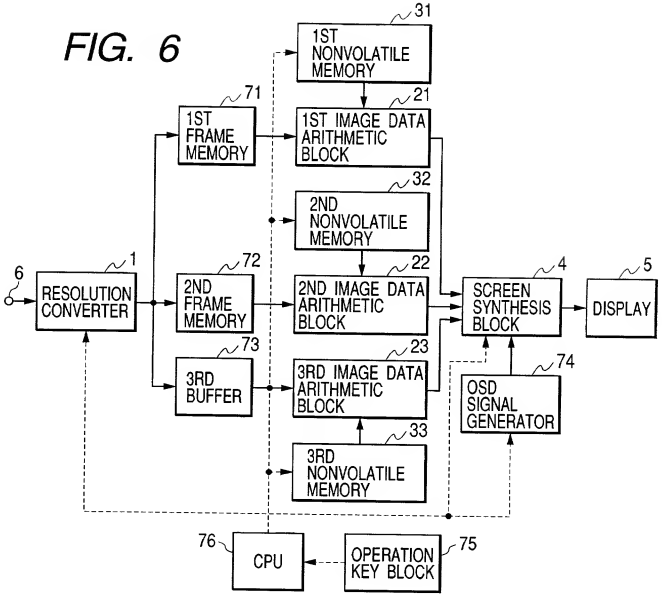
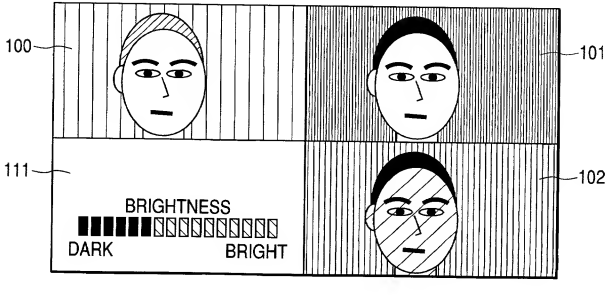

31
1ST NONVOLATILE MEMORY
71
1ST FRAME MEMORY
21
1ST IMAGE DATA ARITHMETIC BLOCK
32
2ND NONVOLATILE MEMORY
6
1
RESOLUTION CONVERTER
72
2ND FRAME MEMORY
22
2ND IMAGE DATA ARITHMETIC BLOCK
4
SCREEN SYNTHESIS BLOCK
5
DISPLAY
73
3RD BUFFER
23
3RD IMAGE DATA ARITHMETIC BLOCK
74
OSD SIGNAL GENERATOR
33
3RD NONVOLATILE MEMORY
76
CPU
OPERATION KEY BLOCK
75

FIG. 7


100
101
111
102
BRIGHTNESS
DARK
BRIGHT